Design of LCC Impedance Matching Circuit for Wireless Power Transfer System Under Rectifier Load

Chenglin Liao, Junfeng Li, and Shufan Li

Abstract—In wireless power transfer system, impedance matching circuits are usually used to match the impedance between actual load and the optimal load, to achieve the maximum transfer efficiency (coil to coil efficiency). It is easily to design impedance matching circuit parameters for linear load. However, the load is always the rectifier one in many applications. The equivalent impedance of rectifier load is complex impedance, which is affected not only by itself, but also by pre-stage circuit, such as impedance matching circuit parameters. It is hard to calculate the equivalent impedance of rectifier load directly to design the impedance matching circuit parameters. This paper investigates the LCC impedance matching design method for secondary side under rectifier load. Firstly, the transfer efficiency characteristic under rectifier load is studied, and the equivalent impedance is analyzed according to transfer efficiency. Then the impedance calculation method of secondary side under rectifier load is derived via formula derivation and Fourier series theory, and the LCC circuit parameters design method is proposed. Lastly, a wireless charging system for electric vehicle is established to verify the method that can transfer 3.3 kW power over 20 cm distance with 92.7% system efficiency (end to end efficiency).

Index Terms—Impedance calculation, LCC Impedance matching, magnetic resonance coupling, optimization, rectifier load.

I. INTRODUCTION

RECENTLY, wireless power transfer technology (WPT) is widely used in charging platforms of mobile device, biomedical implants, wireless sensor networks, electric vehicle, and many other applications [1]-[4].Compared to wired way, WPT is more safe, convenient, automated and environmentally adaptive. In 2007, a new WPT design method via magnetic resonant coupling (MRC) was proposed to enlarge the distance, improve the transmission power and efficiency, especially in middle range charging applications [5], [6].

For a common MRC system with two coils structure, primary coil and secondary coil, the circuit loop of secondary side is working at resonant condition [7], [8], and the transfer efficiency is affected by the coupling coefficient, quality factor, frequency, load and other parameters. There exists the optimum load value to achieve maximum transfer efficiency [9], [10]. However, the actual load is not always equal to the optimum value, so impedance matching circuits are used to match the impedance between actual load and optimum load [11]-[13]. There are many topologies of impedance matching circuit, and the circuit parameters can be easily designed using analytical or numerical method for linear load, such as resistive load, R-C (resistance and capacitance) load and R-L (resistance and inductance) load [14]-[17].

In many applications, such as electric vehicle wireless charger and mobile phone wireless charger, WPT system outputs are direct voltage and current, and always use rectifier circuit to convert high frequency alternating current into direct current, and then supply DC power for actual load, or other DC-DC converters [18]-[20]. Thus the load of secondary coil is a nonlinear rectifier load, and the impedance matching design becomes more complicated than linear load. The equivalent impedance of rectifier load is complex, containing resistive part and imaginary part, and the equivalent value is affected not only by its parameters, but also by the pre-stage impedance matching circuit, which cannot be easily simplified into linear impedance. So it is difficult to design the impedance matching circuit parameters for secondary side coil under rectifier load.

As mentioned in literature [21], a double-sided LCC compensation network and its tuning method was proposed for wireless EV charging system, which ensured the resonant frequency is irrelevant with the coupling coefficient between the two coils and is also independent of the load condition. But it doesn't take the nonlinear rectifier load into consideration. In literature [22], a Series/Series-Parallel topology was analyzed to achieve constant voltage output. In literature [23], the steady state track current was load independent using the LCL-T based impedance matching circuit. Despite various compensation methods, few of them were based on the nonlinear analysis of the rectifier load, instead considering the rectifier load as a resistance load.

This paper theoretically analyzes the working process of rectifier load together with LCC impedance matching circuit, and then deduces the analytical expressions of the voltage and current in circuit at each operation modes using Fourier series theory. Furthermore, the equivalent impedance calculation method of rectifier load is derived based on the fundamental component. Last but not the least, the secondary side impedance matching circuit parameters can be determined to achieve maximum transfer efficiency (coil to coil efficiency) for MRC system in an experimental way.

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II. BASIC ANALYSIS OF WIRELESS POWER TRANSFER

A. Basic Theory Model Under Resistive Load

The simplified typical circuit of WPT system with two coils structure, as Fig. 1 shows, contains voltage source U_l , primary capacitor C_p , primary coil inductor L_p , primary coil resistor R_p , secondary capacitor C_s , secondary coil inductor L_s , secondary resistor R_s , and load $R_L M_{PS}$ is the mutual inductance between primary coil and secondary coil.



Fig. 1. Simplified typical circuit of WPT under resistance load.

The typical circuit can be described in matrix equations (1) [24]. The power loss P_{i} , load power P_{L} , and efficiency η satisfy (2)-(4), where ω is operation radian frequency.

$$\begin{bmatrix} U_{I} \\ 0 \end{bmatrix} = \begin{bmatrix} j\omega L_{p} + 1/(j\omega C_{p}) + R_{p} & j\omega M_{PS} \\ j\omega M_{PS} & j\omega L_{S} + 1/(j\omega C_{S}) + R_{S} + R_{L} \end{bmatrix} \begin{bmatrix} I_{I} \\ I_{2} \end{bmatrix}$$
(1)

$$P_t = I_P^2 R_P + I_S^2 R_S \tag{2}$$

$$P_L = I_S^2 R_L \tag{3}$$

$$\eta = 1 / (1 + P_t / P_L) \tag{4}$$

Where I_P and I_S are the virtual values of the currents of primary and secondary side, respectively.

We use $y = P_t/P_L$, and calculate y in (5), where κ_{PS} is the coupling factor between coils, Q_P and Q_S is quality factor of primary and secondary coil, $Q_{PS} = \omega L_{PS}/R_{PS}$.

$$y = \frac{\left|j\omega L_{s} + 1/(j\omega C_{s}) + R_{s} + R_{L}\right|^{2}}{\omega \kappa_{PS}^{2} L_{s} Q_{P} R_{L}} + \frac{\omega L_{s}}{Q_{s} R_{L}}$$
(5)

So it is obviously that $\eta \propto Q_P$, Q_S , κ_{PS} , and reaches local maximum point at $j\omega L_s + 1/(j\omega C_s) = 0$.

Equation (5) can be simplified into (6) when parameters satisfy $j\omega L_s + 1/(j\omega C_s) = 0$.

$$y = \frac{(R_s + R_L)^2}{\omega \kappa_{PS}^2 L_S Q_P R_L} + \frac{\omega L_S}{Q_S R_L}$$
(6)

We can get the maximum efficiency η when y reaches its minimum point. The maximum condition in (7) is satisfied when R_L equals to the optimum value R_{opt} : $R_{opt} = R_S \sqrt{1 + k_{PS}^2 Q_P Q_S}$.

$$\frac{\partial y}{\partial R_L} = \frac{1}{wk_{PS}^2 L_S Q_P} - \frac{R_S^2}{wk_{PS}^2 L_S Q_P R_L^2} - \frac{wL_S}{Q_S R_L^2} = 0$$
(7)

And then the maximum efficiency η_{max} can be calculated in (8)[25],

$$\eta_{max} = \Delta / \left(\sqrt{1 + \Delta} + 1\right)^2 \tag{8}$$

$$\Delta = k_{PS}^2 Q_P Q_S \tag{9}$$

Therefore, if L_P , L_S , κ_{PS} , R_P , R_S are known, we can obtain the maximum efficiency conditions:

$$\begin{cases} C_S = 1/(\omega^2 L_S) \\ R_L = R_{opt} \end{cases}$$
(10)

B. Efficiency Characteristics Under Rectifier Load

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In many applications, there is not always the resistive load of secondary side coil, but the rectifier load that contains rectifier diode, large capacitance C_L and resistance R_L , as Fig. 2 shows. The resistance R_L is the actual load for users, but the rectifier load is the real load for secondary side coil.



Fig. 2. Typical circuit of WPT system under rectifier load

To study the effect of the rectifier load on the performance of the WPT system, a simulation is conducted in Matlab/Simulink test bench. The efficiency characteristic of WPT system under rectifier load is the same with system under resistance load, as Fig. 3 shows. There also exists the maximum efficiency point



Fig. 3. Efficiency comparison between resistance load and rectifier load, where $C_s = 61.1$ nF, C_s and L_s are working at resonant condition.

and the similar tendency, where parameters as TABLE I show.

Parameter	Value	Parameter	Value
L_P	164.26 <i>uH</i>	L_S	165.69 uH
R_P	$0.101 \varOmega$	R_S	0.129 Ω
M_{PS}	27.04 <i>uH</i>	Frequency	50 kHz
C_{I}	470 uF		

TABLE I WPT system parameter

However, there are different efficiency characteristics between the two load types apparently. Firstly, the optimum R_L for maximum efficiency is different, as Fig. 3 shows, and larger in system under rectifier load. Secondly, at different R_L conditions, as Fig. 4 shows, the optimum capacitance C_s for WPT system under rectifier load is varying, while it is constant in system under resistance load that equals to $1/(\omega^2 L_s)$. According to Fig. 3 and Fig. 4, it can be indicated that the equivalent impedance of rectifier load is complex impedance, which contains real part and imaginary part, and the equivalent value is not only affected by its parameters, but also the pre-stage circuit parameter. When designing the impedance matching circuit between rectifier load and secondary coil, the characteristics must be taken into consideration.



Fig. 4. Effects of C_s on efficiency at different R_L conditions of rectifier load.

III. THEORY ANALYSIS OF PROPOSED IMPEDANCE CALCULATION METHOD FOR RECTIFIER LOAD USING LCC IMPEDANCE MATCHING CIRCUIT

As is shown in section II, and optimum load R_{opt} should be obtained to achieve the maximum efficiency of the WPT system. However, it is obvious from Fig. 1 that the S-S impedance matching method cannot adjust the real part of the secondary side impedance, thus it cannot match the load to R_{opt} unless the real part of the rectifier load equals to R_{opt} . Taking the above analysis into consideration, the LCC impedance matching method is introduced to meet the optimum load R_{opt} for secondary coil for maximum efficiency in WPT system under rectifier load, as Fig. 5 shows, C_s , C_{21} and L_{21} forms LCC impedance matching circuit.



Fig. 5. LCC impedance matching circuit for rectifier load.

For conventional LCC impedance matching method, the rectifier load is seen as a resistance load. A simulation was conducted based on the data shown in TABLE I. Using the conventional LCC impedance matching method, rectifier load and resistance load were matched to the optimum load respectively, as Fig. 6 shows. It can be seen that with the conventional method, a relatively high efficiency can be achieved for resistance load, while for rectifier load, the efficiency is lower except for very small R_L . The main reason for this is that the rectifier load is seen as a resistance load in the conventional method, introducing errors which cannot be neglected. In the following part of this section, the rectifier load will be calculated to solve this problem.



Fig. 6. Efficiency comparison between resistance load and rectifier load for conventional LCC impedance matching method.

A. Circuit Operation Analysis

Due to heavily affected by pre-stage circuit, rectifier load is combined with LCC circuit for impedance calculation. The equivalent impedance Z_{21} in Fig. 5 is analyzed to match R_{opt} via adjusting C_{sy} C_{21} and L_{21} .

Simulations have been conducted to study the circuit operation of the secondary side for LCC compensation method.Fig. 7 shows the operation current and voltage waveforms of secondary side circuit, of which the rectifier works in continuous conduction mode (CCM) (the working condition of the rectifier will be illustrated later). Though affected by diode nonlinear characteristic, the secondary coil current $i_s(t)$ is approximately sinusoidal waveform using LCC impedance matching circuit. We can regard i_s as a sinusoidal current source, $i_s(t)=I_s \sin(\omega t+\delta)$.



Fig. 7. Operation waveforms of LCC and Rectifier circuit (CCM state). δ is the rectifier diodes conduction lag, θ is the conduction angle of rectifier circuit.

The rectifier operation process can be described by four modes as Fig. 8 shows.



Fig. 8. Operation modes of rectifier load. (a) $0 \le \omega t < \theta$; (b) $\theta \le \omega t < \pi$; (c) $\pi \le \omega t < \pi + \theta$; (d) $\pi + \theta \le \omega t < 2\pi$.

(a) When $0 \le \omega t < \theta$:

We can establish the circuit equations as (11)-(14) according to Fig. 8(a). So $u_L(t)$ can be deduced in (15).

$$i_{C21}(t) = C_{21} \frac{du_{C21}(t)}{dt}$$
(11)

$$i_{L21}(t) = \frac{u_L(t)}{R_L} + C_L \frac{du_L(t)}{dt}$$
(12)

$$i_{C21}(t) + i_{L21}(t) = I_s \sin(\omega t + \delta)$$
 (13)

$$u_{C21}(t) = L_{21} \frac{di_{L21}(t)}{dt} + u_L(t)$$
(14)

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$$\frac{d^3 u_L(t)}{dt^3} + A \frac{d^2 u_L(t)}{dt^2} + B \frac{d u_L(t)}{dt} + C u_L(t) = D \sin(\omega t + \delta) (15)$$

Where $A = 1/(C_L R_L)$, $B = 1/(L_{2l}C_{2l}) + 1/(L_{2l}C_L)$, $C = 1/(L_{2l}C_{2l})$ $C_L R_L$, $D = I_S/(L_{2l}C_{2l}C_L)$

We can obtain the characteristic equation as (16), and solve the roots $\lambda_1, \lambda_2, \lambda_3$ in (17)-(18).

$$\lambda^3 + A\lambda^2 + B\lambda + C = 0 \tag{16}$$

$$\lambda_{1} = -\left(\frac{B}{3} - \frac{A^{2}}{9}\right) / \sqrt[3]{\sqrt{\left(\frac{A^{3}}{27} - \frac{AB}{6} + \frac{C}{2}\right)^{2} + \left(\frac{B}{3} - \frac{A^{2}}{9}\right)^{3}}} - \frac{A^{3}}{27} + \frac{AB}{6} - \frac{C}{2}} - \frac{A}{3} + \sqrt[3]{\sqrt{\left(\frac{A^{3}}{27} - \frac{AB}{6} + \frac{C}{2}\right)^{2} + \left(\frac{B}{3} - \frac{A^{2}}{9}\right)^{3}}} - \frac{A^{3}}{27} + \frac{AB}{6} - \frac{C}{2}}$$
(17)

$$\lambda_{2,3} = \left(\frac{B}{3} - \frac{A^2}{9}\right) \left[2*\sqrt[3]{\sqrt{\left(\frac{A^3}{27} - \frac{AB}{6} + \frac{C}{2}\right)^2 + \left(\frac{B}{3} - \frac{A^2}{9}\right)^3} - \frac{A^3}{27} + \frac{AB}{6} - \frac{C}{2}} \right] -\frac{A}{3} - \frac{1}{2}\sqrt[3]{\sqrt{\left(\frac{A^3}{27} - \frac{AB}{6} + \frac{C}{2}\right)^2 + \left(\frac{B}{3} - \frac{A^2}{9}\right)^3} - \frac{A^3}{27} + \frac{AB}{6} - \frac{C}{2}} \pm i\frac{\sqrt{3}}{2} \left[\left(\frac{B}{3} - \frac{A^2}{9}\right) / \sqrt[3]{\sqrt{\left(\frac{A^3}{27} - \frac{AB}{6} + \frac{C}{2}\right)^2 + \left(\frac{B}{3} - \frac{A^2}{9}\right)^3} - \frac{A^3}{27} + \frac{AB}{6} - \frac{C}{2}} + \sqrt[3]{\sqrt{\left(\frac{A^3}{27} - \frac{AB}{6} + \frac{C}{2}\right)^2 + \left(\frac{B}{3} - \frac{A^2}{9}\right)^3} - \frac{A^3}{27} + \frac{AB}{6} - \frac{C}{2}} \right]$$
(18)

It is obvious that λ_2 , λ_3 can be described as $\lambda_{2,3} = \alpha \pm i\beta$. So the solution for homogenous equation of (15) can be described as (19), where C_1 , C_2 , C_3 are coefficients.

$$u_{L}(t) = C_{1}e^{\lambda_{1}} + e^{\alpha t} \left[C_{2}\cos(\beta t) + C_{3}\sin(\beta t) \right]$$
(19)

Based on further analysis, we can know that $\alpha < 0$, and $i\omega$ is not the solution of homogenous form of (15). The particular solution of (15) can be described in (20). The coefficient C_4 and C_5 are calculated by (21). Then $u_L(t)$, $i_{L2l}(t)$, $u_{C2l}(t)$ can be described in (22)-(24) when $0 \le \omega t < \theta$.

$$u_{L}^{*}(t) = C_{4}\cos(\omega t + \delta) + C_{5}\sin(\omega t + \delta)$$
(20)

$$\begin{bmatrix} \omega^3 - B\omega & C - A\omega^2 \\ C - A\omega^2 & -\omega^3 + B\omega \end{bmatrix} \begin{bmatrix} C_4 \\ C_5 \end{bmatrix} = \begin{bmatrix} D \\ 0 \end{bmatrix}$$
(21)

$$u_{L}(t) = u_{L}(t) + u_{L}(t)$$

= $C_{1}e^{\lambda_{1}t} + e^{\alpha t} [C_{2}\cos(\beta t) + C_{3}\sin(\beta t)]$
+ $C_{4}\cos(\omega t + \delta) + C_{5}\sin(\omega t + \delta)$ (22)

$$i_{L21}(t) = e^{\alpha t} (C_L C_3 \beta + C_L \alpha C_2 + C_2 / R_L) \cos(\beta t)$$

+ $e^{\alpha t} (C_L \alpha C_3 - C_L C_2 \beta + C_3 / R_L) \sin(\beta t)$
+ $(C_L C_5 \omega + C_4 / R_L) \cos(\omega t + \delta)$
+ $(C_5 / R_L - C_L C_4 \omega) \sin(\omega t + \delta)$
+ $C_1 e^{\lambda_1 t} (C_L \lambda_1 + 1 / R_L)$ (23)

$$F_{C21}(t) = [(\alpha^2 - \beta^2)C_LC_2 + 2\alpha\beta C_LC_3 + \frac{\alpha C_2 + \beta C_3}{R_L}]e^{\alpha t}\cos(\beta t)$$

$$+ [(\alpha^2 - \beta^2)C_LC_3 - 2\alpha\beta C_LC_2 + \frac{\alpha C_3 - \beta C_2}{R_L}]e^{\alpha t}\sin(\beta t)$$

$$-\omega(C_LC_5\omega + C_4 / R_L)\sin(\omega t + \delta)$$

$$+ \omega(C_5 / R_L - C_LC_4\omega)\cos(\omega t + \delta)$$

$$+ C_1\lambda_1e^{\lambda_1 t}(C_L\lambda_1 + 1 / R_L)$$
(24)

 β in (23) reflects the high frequency component in $i_{L21}(t)$, and

satisfies (25). It is obvious that the high frequency component of diodes current is closely related to L_{21} , and the frequency β can be very high when L_{21} is very low.

$$\beta \approx \sqrt{\frac{1}{L_{21}C_{21}} + \frac{1}{L_{21}C_L} - \frac{1}{3}\left(\frac{1}{C_L R_L}\right)^2} \approx \frac{1}{\sqrt{L_{21}C_{21}}}$$
(25)

(b) When $\theta \le \omega t < \pi$:

At that moment, rectifier diodes turn off, and $u_L(t)$, $u_{C2l}(t)$ can be described in (26)-(27).

$$u_L(t) = u_L(\theta / \omega) e^{(\theta - t)/(\omega R_L C_L)}$$
(26)

$$u_{C21}(t) = \frac{1}{C_{21}} \int_{\theta/\omega}^{t} i_{S}(t) dt + u_{C21}(\frac{\theta}{\omega})$$
(27)

(c) When $\pi \le \omega t < \pi + \theta$:

According to the cycle property, we can substitute t- π/ω for t in (24) to get the expression of $-u_{C21}(t)$ when $\pi \le \omega t \le \pi + \theta$.

(d) When $\pi + \theta \le \omega t < 2\pi$:

We can also substitute t- π/ω for t in (27) to get the expression of $-u_{C2l}(t)$ when $\pi+\theta \le \omega t < 2\pi$.

B. Calculation of Equivalent Impedance Z_{21}

According to circuit theory, expressions of $u_L(t)$, $i_{L21}(t)$, $u_{C21}(t)$ should satisfy initial conditions in (28)-(32), so we can solve C_1 , C_2 , C_3 , δ .

$$i_{L21}(0) = 0 (28)$$

$$i_{L21}(\frac{\theta}{\omega}) = 0 \tag{29}$$

$$u_L(0) = u_L\left(\frac{\pi}{\omega}\right) \tag{30}$$

$$u_{C21}(0) = u_L(0) \tag{31}$$

$$u_{C21}\left(\frac{\pi}{\omega}\right) = -u_L\left(\frac{\pi}{\omega}\right) \tag{32}$$

Therefore, the expressions of $u_L(t)$, $i_{L2l}(t)$, $u_{C2l}(t)$ is determined. We use Fourier series theory to deduce the expression of $u_{C2l}(t)$ in whole cycle in (33).

$$u_{\rm C21}(t) = \frac{a_0}{2} + \sum_{n=1}^{\infty} \left[\sqrt{a_n^2 + b_n^2} \sin(n\omega t + \varphi_n) \right] (n=1,2...)$$
(33)

$$a_n = \frac{\omega}{\pi} \int_{-\pi/\omega}^{\pi/\omega} u_{C21}(t) \cos(nt) dt$$
(34)

$$b_n = \frac{\omega}{\pi} \int_{-\pi/\omega}^{\pi/\omega} u_{C21}(t) \sin(nt) dt$$
 (35)

$$\varphi_n = \arccos \frac{b_n}{\sqrt{a_n^2 + b_n^2}}$$
(36)

In WPT system with optimal LCC impedance matching circuit, $u_{C21}(t)$ is approximately like sinusoidal waveforms, and containing few harmonic distortion. We can use fundamental component to calculate the equivalent impedance of Z_{21} in Fig. 5 by (37).

$$Z_{21} = \frac{U_{C21}}{I_s} = \frac{\sqrt{a_1^2 + b_1^2}}{I_s} e^{j(\varphi_1 - \delta)}$$
(37)

 $Z_{21} = R_{21} + jX_{21}$, where R_{21} and X_{21} are the real and imaginary part of Z_{21} respectively.

Then the equivalent impedance of rectifier load Z_L is computed in (38)

$$Z_{L} = \frac{Z_{21}}{1 - i\omega C_{21} Z_{21}} - i\omega L_{21}$$
(38)

C. Special Operation Mode of LCC

When θ increases to π , the four rectifier operation modes translate into two modes as Fig. 9 shows. And we can also deduce the expression of $u_{C21}(t)$ and Z_{21} using the same method.

$$I_{S} \xrightarrow{L_{21}} I_{121} I_{L} I_{L} I_{RL} I_{RL}$$

Fig. 9. Operation modes of rectifier load when $\theta = \pi$. (a) $0 \le \omega t < \pi$; (b) $\pi \le \omega t < 2\pi$.

D. Analysis of Rectifier Load Equivalent Impedance Z_R

Equivalent impedance Z_L of rectifier load can be calculated based on former analysis. The value of Z_L contains real part Z_{LR} and imaginary part X_{LR} , as Fig. 10-Fig. 11 shows. LCC circuit parameters C_{2l} and L_{2l} heavily affect Z_L value.

According to the results, both real and imaginary part of Z_L is not constant, which is causing problems on designing parameters for impedance matching circuit. As for linear load, we can design impedance matching circuit independently, because the load impedance and target impedance are definite. However, the equivalent impedance of rectifier load is time-varying, so a novel method to design the impedance matching parameters is in evitable to be put forward.



Fig. 10. The impedance impacts of L_{21} and C_{21} on R_{LR} , when R_{L} = 50 Ω .



Fig. 11. The impedance impacts of L_{21} and C_{21} on X_{LR} , when $R_L = 50 \Omega$.

Rectifier load only contains diodes, C_L and R_L , without inductance, but it is interesting that the equivalent impedance model of rectifier load can present *R*-*L* load.

IV. DESIGN OF LCC PARAMETERS UNDER RECTIFIER LOAD

In order to meet the maximum efficiency condition, R_{21} is designed to equal R_{opt} by adjusting C_{21} and L_{21} , and secondary side circuit is tuned to resonant by C_s . According to the theory analysis, C_s is not necessary in some cases, and the impedance matching circuit can be translated into LCL topology.

When designing the LCC parameters L_{21} , C_{21} and C_5 , there are many combinations satisfying the maximum efficiency condition. In order to choose one combination for WPT system, L_{21} or C_{21} can be determined firstly, and then compute others. According to the effect of L_{21} and C_{21} on R_{21} , as Fig. 12 shows, the value of C_{21} is more flexible to regulate R_{21} .

The variable β in (23) reflects the harmonic current in rectifier diodes, as Fig. 13 shows, which is even determined by L_{21} and C_{21} . When β becomes larger, the loss of diodes is obviously increasing because of the rectifier works from continuous



Fig. 12. The impedance impacts of L_{21} and C_{21} on R_{21} , when $R_L = 50 \Omega$.



Fig. 13. The frequency impacts of L_{21} and C_{21} on high-frequency β (kHz), when $R_{L}{=}\,50~\Omega.$

conduction mode (CCM) to discontinuous conduction mode (DCM), meanwhile the peak current becomes larger, as Fig. 14 shows.



Fig. 14. The current wave of rectifier diode using LCC impedance matching circuit. L_s = 165.7 uH, C_s = 117.3 nF, C_{21} = 161.9 nF, f=50 kHz.

It is obvious that we can increase L_{21} or C_{21} to decrease β value, and L_{21} is more suitable to enlarge the scope of impedance matching for rectifier load. Therefore, we can determine L_{21} firstly, and then compute C_{21} and C_s . L_{21} should be large enough to be optimized according to actual need, therefore, the initial value of L_{21} should be its maximum value, which can be determined according to experiences. When L_{21} is determined, we can analyze the effects of C_{21} on R_{21} , and decide its value according to $R_{21} = R_{opt}$. Then C_s can be tuned to resonant for secondary side circuit loop. If the conditions " $R_{21} = R_{opt}$ " or " Z_{21} = R_{opt} " cannot be satisfied with the preset value of L_{21} , then its value should be decreased by 0.1µH in a loop until the conditions above are satisfied. A complete designing process of the secondary side LCC parameters is showed in Fig. 15.

After determining the value of the secondary side LCC network, the value of the primary side LCC network can be designed according to the conventional LCC compensation method [21]. It should be noticed that the value of "L", namely the inductance in the LCC network of the primary side, should be slightly larger(usually 10%~20%) than its theoretical value to guarantee an inductive load of the inverter, thus satisfying the ZVS condition of the system.



Fig. 15. Flowchart of the designing process of the LCC parameters.

V. EXPERIMENTAL VERIFICATION

Experiments are conducted to verify the design method of LCC impedance matching circuit under rectifier load. The WPT system is established as Fig. 16 shows, containing DC voltage source, voltage source inverter, primary coil and secondary coil, and dual-side LCC impedance matching circuits, rectifier circuit and load. The system parameters are shown in TABLE I.



Fig. 16. WPT system circuit.

A. Impedance Matching for Different RL

According to WPT parameters in TABLE I, the optimal load for secondary coil and maximum efficiency can be theoretically calculated: $R_{opt} = 9.6 \Omega$, and $\eta_{max} = 97.35\%$. According to the proposed LCC impedance matching method, the rectifier load with different R_L can be matched by parameters in Fig. 17, where $L_{21} = 86.1$ uH. The transfer efficiency after LCC impedance matching is improved as Fig. 18 shows.

The transfer efficiency of the WPT system with the conventional C impedance matching(as Fig. 2 shows), the conventional LCC impedance matching [21] and the proposed LCC impedance matching is simulated separately. Due to the power losses of the converters and other components in the circuit, the efficiency does not reach the theoretical maximum efficiency η_{max} . However, it is obvious that compared to the conventional



Fig. 17. The capacitors value and R_{21} at different actual load R_L conditions.



Fig. 18. The transmission efficiency η at different actual load R_L conditions.

impedance matching methods, the efficiency of the proposed impedance matching method is higher for different values of R_L , especially those far away from the optimum value.

B. 3.3 kW WPT System for EV Charging

The proposed LCC impedance matching method is applied to designing wireless charging system for electrical vehicle(EV), which can transfer 3.3 kW power over 20 cm distance. The coil subsystem of EV wireless charging system contains magnetic disk, coil layer and steel plate, as Fig. 19 shows. The steel plate is used to mimic the eddy current effect of EV chassis on coil. The magnetic disk adopts silicon steel sheet, which can not only decrease the eddy current loss, but also improve the quality factor and coupling coefficient.



Fig. 19. Photo of wireless charging system.

Both primary and secondary coil is the same sizes, and wound by Litz line, as Fig. 20(a) shows.



Fig. 20. Coil and experiment result.

In our system, the battery is charging from 320 V to 350 V. When charging at the normal power, the charging voltage is 336 V, the charging current is about 9.8 A, and the equivalent R_L is about 34 Ω . Thus the secondary side parameters can be determined. Even more, primary side circuit parameters can be theoretically computed to meet the nominal power output after calculating the equivalent impedance of secondary side in TA-BLE II. WT1800 power analyzer is used to measure the system efficiency (end to end efficiency), as Fig. 20(b) shows, where Urm3, Irm3 and P3 are the input voltage, current and power, while Urm4, Irm4 and P4 are the output voltage, current and power, and $\eta 1$ is the system efficiency. It can be seen that the system efficiency is about 92.7%.

TABLE II WIRELESS CHARGING SYSTEM PARAMETERS

Parameter	Value	Parameter	Value
L_{10}	67.7 uH	L_{21}	86.1 <i>uH</i>
C_{10}	266.6 nF	C_s	112.75 nF
C_{12}	86.3 nF	C_{2l}	175.7 nF
R_L	34 Ω	Coil Size	40 cm*40 cm

VI. CONCLUSION

This paper theoretically analyze the operation mechanical of secondary side LCC circuit under rectifier load, and then investigates the equivalent complex impedance calculation method for rectifier load, which is significant for theoretical designing to satisfy the maximum efficiency and normal power output. The proposed LCC impedance matching method can effectively improve transfer efficiency, especially when R_L is far away from optimum value. Typical WPT system researched in this paper, the optimum value of R_L is about 12 Ω , and the transfer efficiency gets the maximum value 97.2% without LCC impedance matching is obviously higher than the typical one. When $R_L > 30 \Omega$, transfer efficiency with LCC is about 2% higher, and When $R_L > 60 \Omega$, transfer efficiency with LCC is about 4% higher.

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